

REMARKS

Claims 3 and 7-10 have been canceled. Claims 11 and 12 have been amended. No new matter has been added. Claims 1-2, 4-6, and 11-12 remain in the application. Reconsideration and reexamination is respectfully requested.

In paper 5, the examiner required designation of figures 3A, 3B, 4A, and 4B as prior art. Applicant respectfully traverses. Each of figures 3A, 3B, 4A, and 4B includes a GOTL (304, 312, 404, 410), which is not known in the prior art.

In paper 5, the drawings were objected to as not showing every feature specified in the claims. Applicant respectfully traverses. The figures include a GOTL. Each of the features specified as not illustrated are contained in a GOTL. From page 7, lines 3-5, a GOTL includes a list of tags and their owners for all the owned cache lines in the system. From page 5, lines 1-2, unowned lines are not in the GOTL. In figures 1 and 2, unowned lines are not referenced in the GOTL. From page 1, line 24, a tag is part of an address. Regarding claim 3 ("single bit"), claim 3 is canceled without prejudice. Regarding claim 6, in figure 3A, every CPU has a copy of the GOTL.

In paper 5, claims 1-6 were allowed, and claims 11-12 were allowable. Claims 7-10 have been canceled without prejudice. Claims 11 and 12 have been rewritten in independent form.

The following comments are in regard to other art made of record by the examiner.

U.S. Patent Number 5,995,967 does not teach or suggest a GOTL.

Entry of this amendment is respectfully requested. This application is considered to be in condition for allowance and such action is earnestly solicited.

Respectfully submitted,

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